

U.S. DEPARTMENT OF COMMERCE  
PATENT AND TRADEMARK OFFICELIST OF REFERENCES CITED BY APPLICANT(S)  
(Use several sheets if necessary)ATTY DOCKET NO.  
**36j.p159**APPLICATION NO.  
**Not yet assigned**APPLICANT  
**Manish Kulkarni**FILING DATE  
**Herewith**GROUP  
**Not yet assigned**

68991/60  
 09/19/98  
 11/20/98  
 JCI:3 U.S. PTO  
 11/20/98

## U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
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## FOREIGN PATENT DOCUMENTS

DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES/NO/ OR ABSTRACT

## OTHER DOCUMENT(S) (Including Author, Title, Date, Pertinent Pages, Etc.)


EXAMINER

Joseph R. Phyna

DATE CONSIDERED

8/3/01

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FORM PTO 1449 (modified)  U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE  LIST OF REFERENCES CITED BY APPLICANT(S) (Use several sheets if necessary)		ATTY DOCKET NO. <b>36J.P159</b>		APPLICATION NO.	
		APPLICANT <b>Manish Kulkarni</b>			
		FILING DATE <b>November 20, 1998</b>		GROUP	

  

U.S. PATENT DOCUMENTS							
*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE	

  

FOREIGN PATENT DOCUMENTS							
DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES/NO/ OR ABSTRACT		

  

OTHER DOCUMENT(S) (Including Author, Title, Date, Pertinent Pages, Etc.)	
JP	Betz, Mark, "VGA Palette Mapping Using BSP Trees: A multidimensional cousin of the binary tree", <u>Dr. Dobb's Journal</u> , Vol. 18, No. 7, p. 28, July 1993
JP	"Local Minimization and Steepest Descent", < <a href="http://www.stanford.edu/class/cs205/notes/book/node25.html">http://www.stanford.edu/class/cs205/notes/book/node25.html</a> >, (visited November 20, 1998)
JP	"Newton's Method", < <a href="http://www.stanford.edu/class/cs205/notes/book/node26.html">http://www.stanford.edu/class/cs205/notes/book/node26.html</a> >, (visited November 20, 1998)
JP	"Simulated Annealing", < <a href="http://petaxp.rug.ac.be/~erik/research/research-part2.html">http://petaxp.rug.ac.be/~erik/research/research-part2.html</a> >, (visited November 20, 1998)
JP	"Simulated Annealing Using Cauchy Cooling", < <a href="ftp://ftp.taygeta.com/pub/Forth/Unreviewed/simannl.seq">ftp://ftp.taygeta.com/pub/Forth/Unreviewed/simannl.seq</a> >, (visited November 20, 1998)
EXAMINER	<div style="display: flex; justify-content: space-between;"> <span><i>Joseph R. Phypre</i></span> <span>DATE CONSIDERED <u>8/3/01</u></span> </div>

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*2/4/99*

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PATENT AND TRADEMARK OFFICELIST OF REFERENCES CITED BY APPLICANT(S)  
(Use several sheets if necessary)ATTY DOCKET NO.  
**36J.P159**APPLICATION NO.  
**09/196,689**APPLICANT  
**Manish Kulkarni**FILING DATE  
**November 20, 1998**GROUP  
**2722**

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*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
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## OTHER DOCUMENT(S) (Including Author, Title, Date, Pertinent Pages, Etc.)


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Sheet 1 of 1